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Term	Documents
SHARED.DWPI,TDBD,EPAB,JPAB,USPT.	83978
SHARED	0
(SHARED ADJ2 21).USPT,JPAB,EPAB,DWPI,TDBD.	9

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USPT,JPAB,EPAB,DWPI,TDBD	shared adj2 l21	9	<u>L22</u>
USPT,JPAB,EPAB,DWPI,TDBD	(SDRAM or SSRAM or (synchronous adj2 memory))	3550	<u>L21</u>
USPT,JPAB,EPAB,DWPI,TDBD	118 and (SDRAM or SSRAM or (synchronous adj2 memory))	0	<u>L20</u>
USPT,JPAB,EPAB,DWPI,TDBD	5960458.pn.	2	<u>L19</u>
USPT,JPAB,EPAB,DWPI,TDBD	(shared adj3 clock) same (agent or processor or controller or cpu)	54	<u>L18</u>
USPT,JPAB,EPAB,DWPI,TDBD	shared same clock same (agent or processor or controller or cpu)	1293	<u>L17</u>
USPT,JPAB,EPAB,DWPI,TDBD	6163831[uref]	0	<u>L16</u>
USPT	5349449.pn.	1	<u>L15</u>
USPT	5361369.pn.	1	<u>L14</u>
USPT	5613136.pn.	1	<u>L13</u>
USPT	5960458.pn.	1	<u>L12</u>
USPT	6006303.pn.	1	<u>L11</u>
USPT,JPAB,EPAB,DWPI,TDBD	19 and l8	6	<u>L10</u>
USPT,JPAB,EPAB,DWPI,TDBD	(controller or processor or cpu or agent) same (send or send??? or provide or provid??? or transfer or transferring) same clock same (first or primary or master or slave) same (second or secondary or master or slave)	4226	<u>L9</u>
USPT,JPAB,EPAB,DWPI,TDBD	shared same ((synchronous adj3 memory) or SDRAM or SSRAM)	96	<u>L8</u>
USPT,JPAB,EPAB,DWPI,TDBD	shared same ((synchronous adj3 memory) or SDRAM or SSRAM)	96	<u>L7</u>
USPT,JPAB,EPAB,DWPI,TDBD	5463755.pn.	3	<u>L6</u>
USPT,JPAB,EPAB,DWPI,TDBD	5933855.pn.	2	<u>L5</u>
USPT,JPAB,EPAB,DWPI,TDBD	6052756.pn.	2	<u>L4</u>
USPT,JPAB,EPAB,DWPI,TDBD	6101146.pn.	2	<u>L3</u>
USPT,JPAB,EPAB,DWPI,TDBD	4476527.pn.	3	<u>L2</u>
USPT,JPAB,EPAB,DWPI,TDBD	5999197.pn.	2	<u>L1</u>